



S/N 10/057,225

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Leonard Forbes et al.	Examiner:	Michael Trinh
Serial No.:	10/057,225	Group Art Unit:	2822
Filed:	January 25, 2002	Docket:	303.506US4
Title:	METHOD OF FABRICATING A TRANSISTOR ON A SUBSTRATE TO OPERATE AS A FULLY DEPLETED STRUCTURE		

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AMENDMENT UNDER 37 C.F.R. § 1.312

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In response to the Notice of Allowance and Notice of Allowability mailed April 22, 2005,  
please amend as follows: